**MuffaKham Jah College of Engineering and Technology**

**Electronics and Communication Engineering Department**

**SECTION-B**

**SUBJECT: COMPUTER ORGANIZATION AND ARCHITECTURE CODE:PC404EC**

**CLASS TEST-2**

**NOTE:**

**1. ANSWER ALL QUESTIONS FROM PART-A**

**2. ANSWER ANY TWO QUESTIONS FROM PART-B**

**PART-A**

**1. Explain the use of following registers: i) PC ii) MAR iii) IR [CO3] [2M]**

**2. Draw the timing diagram of a Strobe control method of I/O interface. [CO4] [2M]**

**3. Write the need for virtual memory concept. [CO5] [2M]**

**PART-B**

**4. Explain one-address,2-address and 3-address instructions related to CPU organization   
 [CO3] [7M]**

**5. Explain the operation of daisy chaining method of priority interrupt. [CO4][7M]**

**6. What are the three basic techniques to perform Input/Output operations? [CO4] [7M]**

**Course Coordinator**

**Mohd. Zakir Hussain**

**Asst.Prof., ECED.**

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| **Class Test Q.No.** | **1** | **2** | **3** | **4** | **5** | **6** |
| **Marks (Xi)** | **2** | **2** | **2** | **7** | **7** | **7** |
| **Bloom’s Score (Si)** | **3** | **4** | **2** | **4** | **3** | **4** |
| **Xi\*Si** | **6** | **8** | **4** | **28** | **21** | **28** |

**BI=4.75**